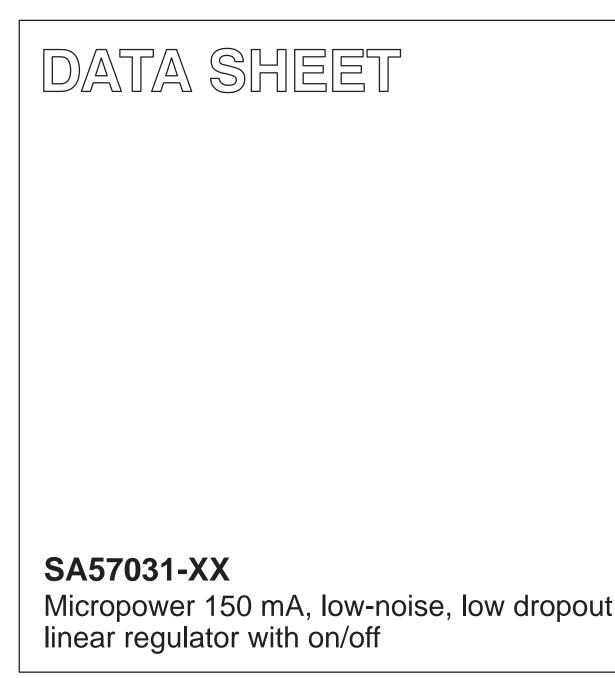
INTEGRATED CIRCUITS



Product data

2003 Feb 24



SA57031-XX

GENERAL DESCRIPTION

The SA57031 is a 150 mA, fixed output voltage regulator designed to provide very low dropout voltage and low noise in battery powered and portable applications. The output voltage is preset to voltages between 1.8 to 5.0 Volts, in 100 mV increments.

The SA57031 achieves excellent performance in all specifications such as excellent power supply ripple rejection at low frequency, low supply-current, low dropout voltage, low power consumption, extremely small size with chip scaled package (CSP), extremely low output noise voltage, very fast start-up response time, precision output, fast dynamic response to line and load regulation, etc. This regulator consists of an internal voltage reference, and current limit.

The SA57031 is housed in a WL-CSP4 package (see dimensional drawing for layout).

FEATURES

- Many Preset Output Voltages: 1.8 to 5.0 V in 0.1 V increments
- Wafer-level chip scale package (WL-CSP4)
- Extremely low PSRR: 80 dB typical at 1 kHz
- Low Dropout Voltage: 100 mV (typ) @ 50 mA output current
- Very Low Consumption Current: 100 mA typ. (when not loaded), 0.1 mA max. (when off)
- High precision Output Voltage: ±2%
- Output Current Capacity: 150 mA (typ)
- Low Noise: 30mVrms @ 10 Hz to 100 kHz
- Good Line Regulation: 0.01%/V typ.
- Good Load Regulation: 0.003%/mA typ.

Bottom View

- Low Temperature Drift Co-efficient to V_O: ±100 ppm/°C
- Built-in Current Limiter
- Wide Operating Temperature Range: -40 to +85 °C

APPLICATIONS

- Cellular Phones, Cordless Phones and 2-way Radios
- Electronic Notebooks, PDAs and Palmtop Computers
- Cameras, VCRs and Camcorders
- PCMCIA Cards
- Modems
- Battery-powered or Hand-held Instruments

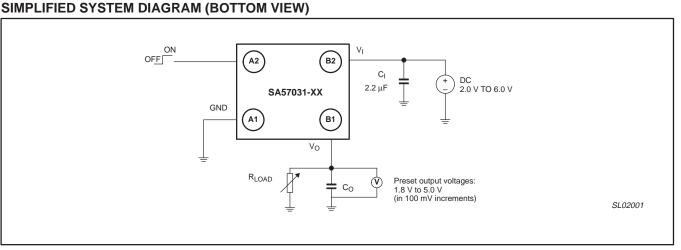


Figure 1. Simplified system diagram.

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ORDERING INFORMATION

TYPE NUMBER	PACKAGE		TEMPERATURE	
	NAME	DESCRIPTION	RANGE	
SA57031- XX UK	WL-CSP4	wafer level, chip-scale package; 4 bumps	–40 to +85 °C	

NOTE:

The device has five voltage output options, indicated by the **XX** on the Type Number. Other voltages, between 2.5 to 5.0 V in 100 mV increments, are available on request.

ХХ	VOLTAGE (Typical)	
18	1.8 V	
24	2.4 V	
28	2.8 V	

PIN CONFIGURATION (TOP VIEW)

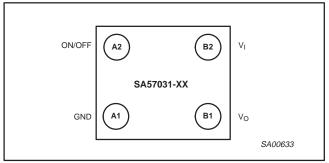


Figure 2. Pin configuration.

MAXIMUM RATINGS

Symbol	Parameter		Мах	Unit
Tstg	Storage temperature range	-40	+150	°C
Toper	Operating Temperature Range	-40	+85	°C
VI	Supply voltage	-0.3	+7	V
ю	Output Current		200	mA
Р	Power dissipation		190	mW

PIN DESCRIPTION

PIN	SYMBOL	DESCRIPTION
A1	GND	Device ground
B1	Vo	Voltage output
B2	VI	Voltage input
A2	ON/OFF	Control pin. Connect to VI if not used.

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CHARACTERISTICS

Tamb = 25 °C; VI = $V_O(nom)$ +1 V; VON/OFF = 2 V; IO = 1 mA unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CC}	No-load input current	I _O = 0 mA	_	45	100	μA
		I _O = 50 mA	-	55	-	
		I _O = 150 mA	-	60	-	
I _{CC} (OFF)	Input current (off)	VON/OFF = 0.25 V	_	0.1	1	μΑ
lq	Quiescent Current	I _O = 0 to 150 mA	_	100	190	μA
ΔV_{LNR}	Line regulation	$V_{I} = VO + 0.5 V \text{ to } 6 V$	_	5	10	mV
		I _O = 1 to 50 mA	_	10	25	
ΔV_{LDR}	Load regulation	I _O = 1 to 100 mA	_	20	50	mV
		I _O = 1 to 150 mA	_	30	70	1
ΔVΟ/ΔΤ	VO Temperature coefficient	Tj = −30 to +85 °C	_		_	ppm/°C
	_	f = 1 kHz; I_O = 1 mA; Vripple = 0.5 V; V _O = 1.8 to 4.0 V	_	80	_	dB
PSRR	Power supply ripple rejection	f = 1 kHz; I _O = 1 mA; Vripple = 0.5 V; V _O = 4.1 to 5.0 V	-	70	_	
Vn	Output noise voltage	fBW = 20 to 80 kHz; $C_0 = 2.2 \ \mu F$	_	30	_	μVrms
I _{ON/OFF}	Output short-circuit current	V _I = VO +1 V ; VO = 0 V	_	30	_	mA
V _{ON/OFF(H)}	ON/OFF HIGH threshold voltage		1.5	_	VI	V
V _{ON/OFF(L)}	ON/OFF LOW threshold voltage		0	_	0.25	V
R _{ON/OFF}	ON/OFF Pull-down resistance		2.5	5	15	MΩ
V _O	Output voltage (nominal)		x0.98	nom	x1.02	V
V _{dropout}	Dropout voltage	$V_0 = 2.0$ to 2.3	-	0.25	0.35	V
		V _O = 2.4 to 2.7	-	0.24	0.29	
		$V_0 = 2.8 \text{ to } 5.0$	-	0.23	0.26	7

PRESET OUTPUT VOLTAGES

Part Number	Min	Тур	Max
SA57031-25UK	2.450	2.500	2.550
SA57031-28UK	2.744	2.800	2.856
SA57031-30UK	2.940	3.000	3.060
SA57031-33UK	3.234	3.300	3.366
SA57031-36UK	3.528	3.600	3.672

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TYPICAL PERFORMANCE CURVES

Shown for the SA57031-28UK only; $T_{amb} = 25 \text{ °C}$; $V_I = V_O + 1 \text{ V}$; $V_{On/Off} = V_I$; $C_{in} = 2.2 \mu\text{F}$, $C_O = 2.2 \mu\text{F}$ unless otherwise specified

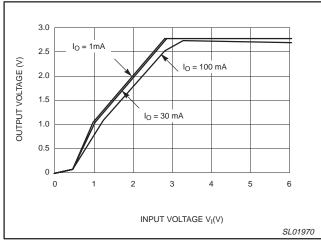


Figure 3. Output voltage versus input voltage.

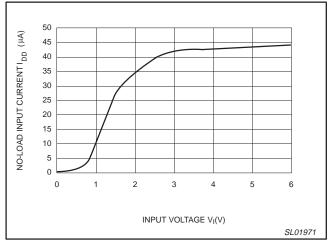


Figure 4. No-load input current versus input voltage.

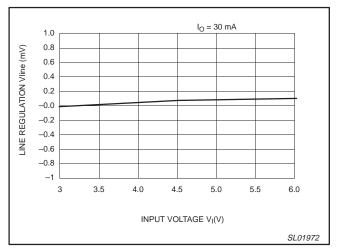


Figure 5. Line regulation versus input voltage.

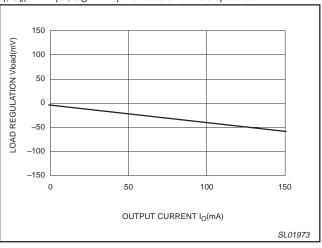


Figure 6. Load regulation versus load current.

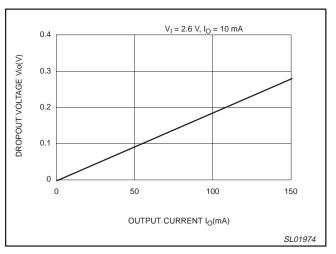


Figure 7. Dropout voltage versus output current.

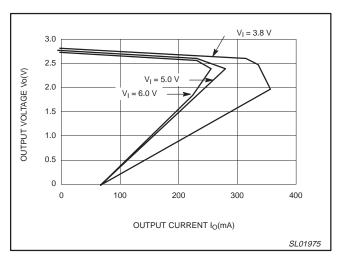


Figure 8. Current limit versus output current.

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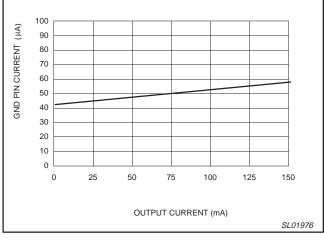


Figure 9. Ground pin current.

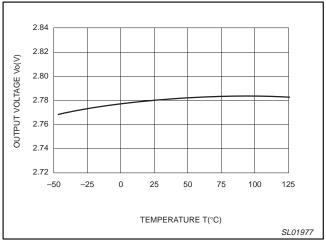


Figure 10. Output voltage versus ambient temperatures.

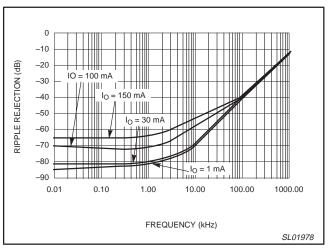


Figure 11. Ripple rejection.

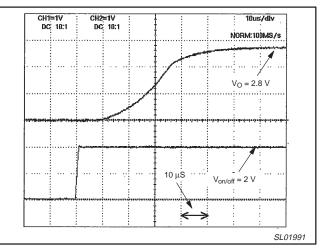


Figure 12. On/off control pin.

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Micropower 150 mA, low-noise, low dropout linear regulator with on/off

CH1=10mV AC CH2=2V DC 200us/div NORM:5MS/s V1 = 3.8 V V1 = 4.8 V V1 = 4.8 V V1 = 3.8 V V1 = 4.8 V V1 = 4.8 V V1 = 200 µS V1 = 2 V/div V2 = 10 mV/div Z00 µS V1 = 2 V/div V2 = 10 mV/div

Figure 13. Line transient response.

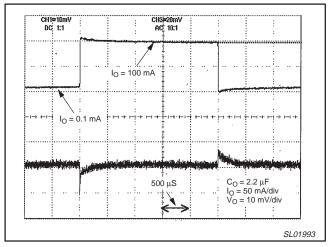


Figure 14. Load transient response.

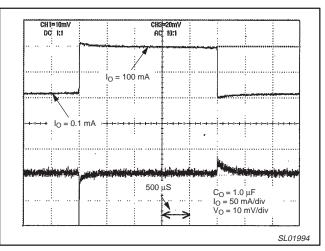


Figure 15. Load transient response.

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TECHNICAL DISCUSSION

As illustrated in Figure 16, the SA57031 consists of a band-gap voltage reference, an error amplifier, P-channel pass transistor, current limit circuit and an internal feedback voltage divider. The output voltage is fed back through an internal resistor voltage divider connected to the VO pin.

The reference is connected to the error amplifier's inverting input. The error amplifier compares the reference with the feedback

voltage and amplifies the difference. If the feedback voltage is lower than the reference voltage, the pass transistor's gate is pulled lower, which allows more current to pass to the output and increase the output voltage. If the feedback voltage is too high, the pass transistor gate is pulled up, allowing less current to pass to the output, resulting in a decrease in output voltage.

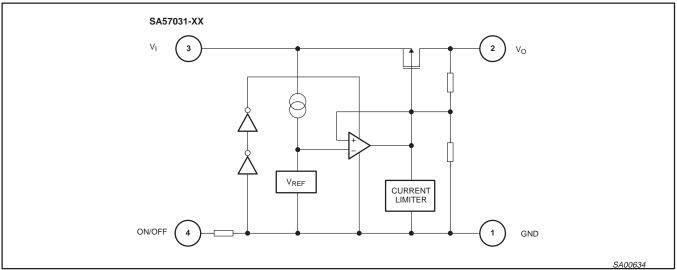


Figure 16. Block diagram (bottom view).

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APPLICATION INFORMATION

The following points have to be kept in mind for good performance while using these devices.

Input Capacitor

An input capacitor of 2.2 μ F is required between the SA57031 input pin and the circuit ground. This capacitor must be located as close as possible to the VI or GND pin (not more than 1 cm away from the pin) and returned to a clean analog ground. Any good quality ceramic, tantalum, or film capacitor will work.

Output Capacitor

Phase compensation is used for securing stable operation even if the load current varies. For this reason, an output capacitor with good frequency characteristics is needed. Set it as close to the circuit as possible and make the wiring as short as possible. The value of the output capacitance has to be at least 2.2μ F and has to have B-type temperature characteristics.

ON/OFF (Chip Enable) Pin

The ON/OFF pin must be actively terminated, not floating. If the function is not needed, the pin should be tied to VI to permanently enable the device.

Line Impedance of VI and GND

The VI and GND lines should be sufficiently wide to minimize impedance. If the impedance of these lines is high, there is a chance to pick up noise or malfunction.

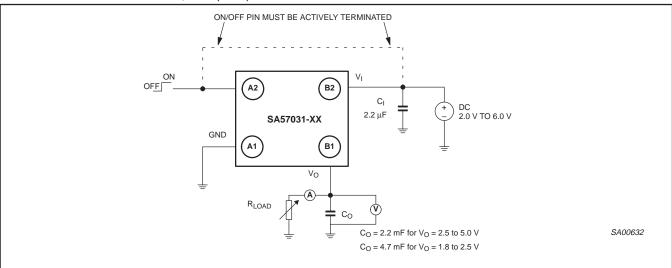


Figure 17. Typical application circuit.

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PACKING METHOD

The SA57031-XX is packed in reels, as shown in Figure 18.

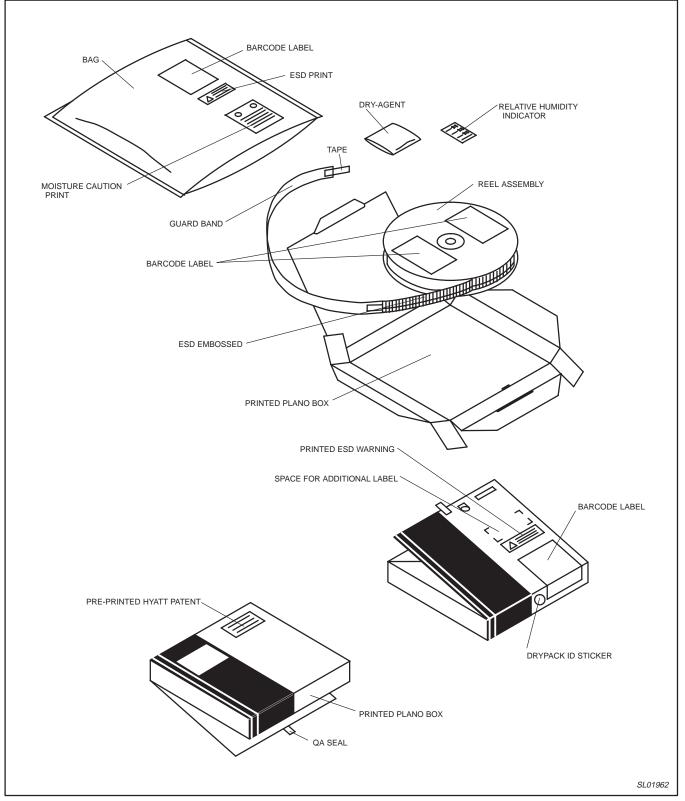
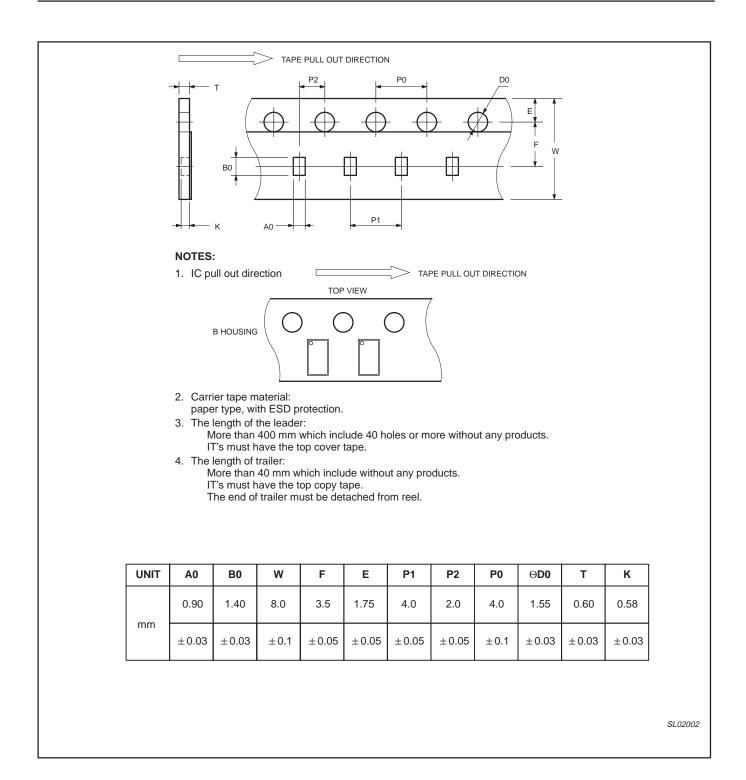


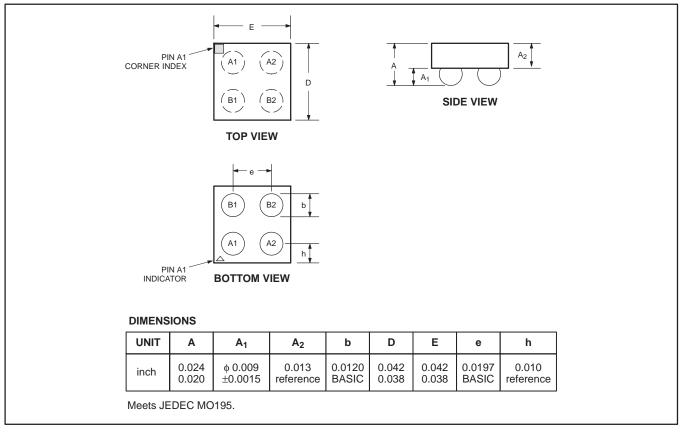
Figure 18. Tape and reel packing method



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WL-CSP4: wafer level, chip-scale package; 4 bumps



Product data

REVISION HISTORY

Rev	Date	Description	
_1	20030224	Product data (9397 750 11001); ECN 853-2361 28634 dated 2002 Jul 18	

SA57031-XX

Data sheet status

Level	Data sheet status ^[1]	Product status ^{[2] [3]}	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

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Contact information

For additional information please visit http://www.semiconductors.philips.com. Fax:

Fax: +31 40 27 24825

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